VX-1337

3 Input Turbo-Encabulator

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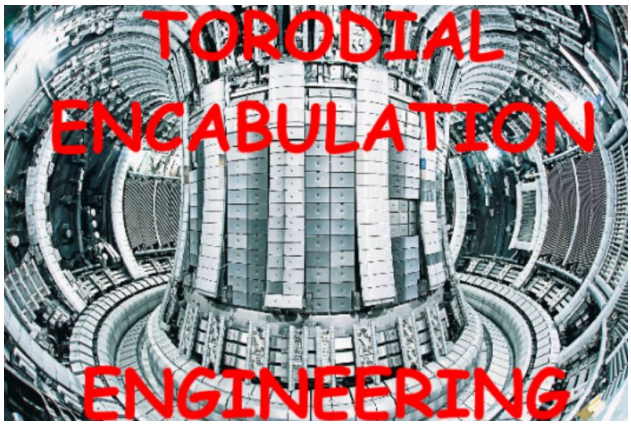


1. Description

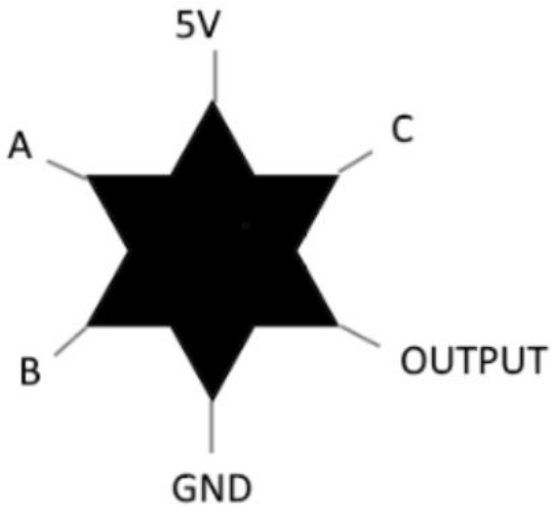
The VX-1337 Turbo-Encabulator is a simple 3 input 1 output static CMOS circuit, using a PDN and PUN this device can provide an output for all 8 different combinations of inputs. It's unique logic function can provide the specific outputs magnitudes faster than the common idea of using an arduino to program a 3 input logic function.

2. Features

- Wicked fast
- Low power usage
- 3 WHOLE inputs
- Low Teraski-Buttersworth Tau constant
- Low cross phasing between mutually inducting cross phasors.
- Optimized circuit for muon fibrillation cascade amplifiers.

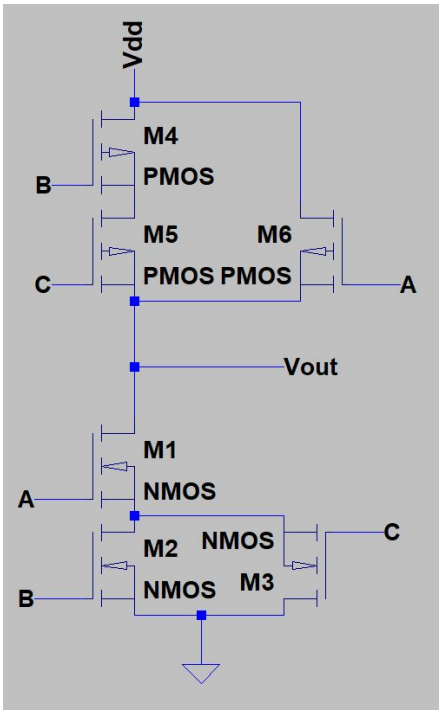
3. Applications

- 11110100 output circuit
- Electric hammer
- Plesmiograph 6-echelon machina filter
- Z axis Jaeger Harmonizer
- Ambivulating oscillator
- Biactrator germanium generator
- Thermionic Tube stabilizers
- Vector correcting ionic thrust modulators
- Gaussian shade amalgamation
- Toaster

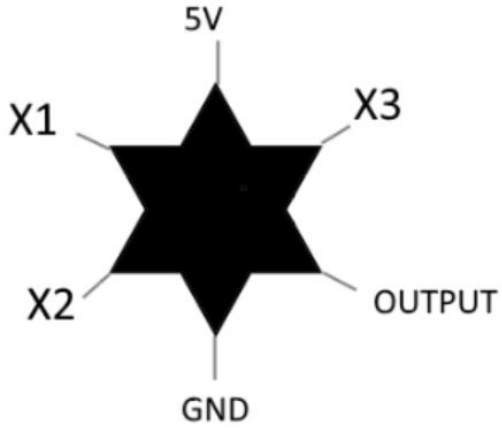


VX-1337 in its most common hex-star DIP package, custom breadboard sold seperatley.

4. Equivalent Schematic



5. Pin Configuration and Functions



$X1$	$X2$	$X3$	Z
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	0

Pins X1 through X3 are the inputs, while Z is the outputs

6. Specifications

6.1. Static Characteristics

	Minimum Voltage	Maximum Voltage	Typical Voltage
LO/HI Voltage Threshold	1.0	2.0	1.4

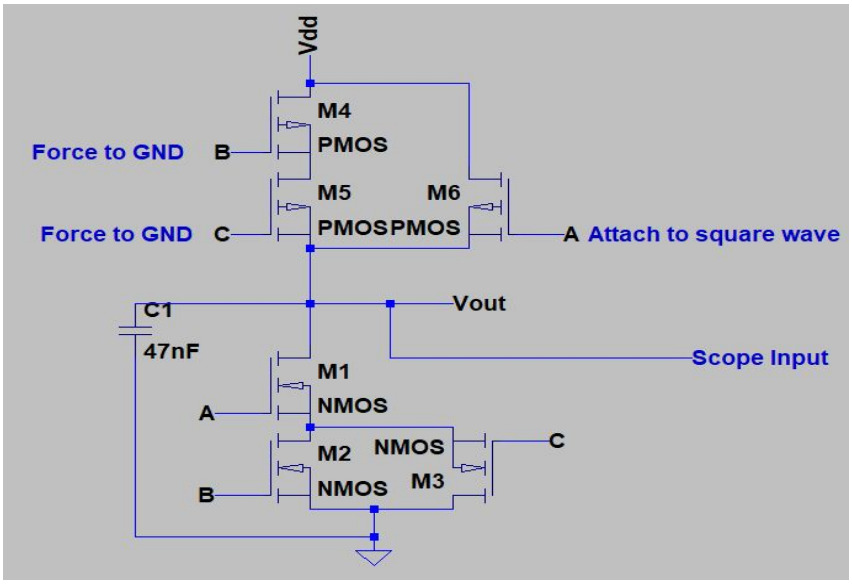
6.4. Dynamic Characteristics

X_1	X_2	T_{rise} (μs)	$T_{P(LH)}$ (μs)	T_{fall} (ns)	$T_{P(HL)}$ (ns)
000	100	2.608	1.239	528	274
100	101	5.251	2.782	584	257
010	110	2.950	1.402	642	284
101	111	5.573	2.818	722	275
011	111	2.664	1.236	642	246

Table 2: 10% to 90% rise and fall times and propagation delays for the full static CMOS logic gate

7. Parameter Measurement Information

Test setup to test all 1 input change rise,fall, and propagation delays.



E.g. this circuit was used to test the 000->100 values.