

#1 / Layer thickness and spacing for C5N process

Layer spacing: inter-layer capacitance

$$C = \frac{\epsilon_r \cdot \epsilon_0 \cdot A}{d}$$

A - area

d - plate separation

(no fringe fields included)

— table uses $\text{aF}/\mu\text{m}^2$, $\rightarrow$ convert units to aF and μm .

$$\epsilon_r (\text{SiO}_2) = 3.9$$

$$\epsilon_0 = 8.854 \times 10^{-14} \frac{\text{F}}{\text{cm}} \cdot \frac{10^2 \text{ cm}}{\text{m}} \cdot \frac{10^{-6} \text{ m}}{\mu\text{m}} \rightarrow X \cdot 10^{-15} \frac{\text{F}}{\mu\text{m}} \rightarrow \frac{\text{aF}}{\mu\text{m}}$$

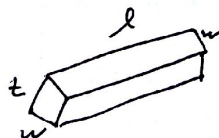
$$\epsilon_{\text{total}} = 34.5 \frac{\text{aF}}{\mu\text{m}}$$

solve

$$d = \frac{\epsilon_{\text{total}} \cdot A}{C} = \frac{34.5 \frac{\text{aF}}{\mu\text{m}}}{X \frac{\text{aF}}{\mu\text{m}^2}} = \boxed{\frac{34.5}{X} \mu\text{m}}$$

X $\rightarrow$ inter-layer C
in $\text{aF}/\mu\text{m}^2$ resistivity $\rightarrow$ thickness

$$R = \rho \frac{l}{A} = \rho \frac{l}{w \cdot t}$$



$$R \cdot t = \frac{\rho \left(\frac{\text{cm}}{\square} \right)}{R \left(\frac{\square}{\square} \right)}$$

$$\rho_{\text{Al}} = 2.82 \times 10^{-8} \Omega \cdot \text{m} \cdot \frac{10^9 \text{ nm}}{\text{m}}$$

$$\rho_{\text{Al}} = 2.82 \times 10^{-1} \Omega \cdot \text{nm}$$

$$\rho_{\text{Al}} = 28.2 \Omega \cdot \text{nm}$$