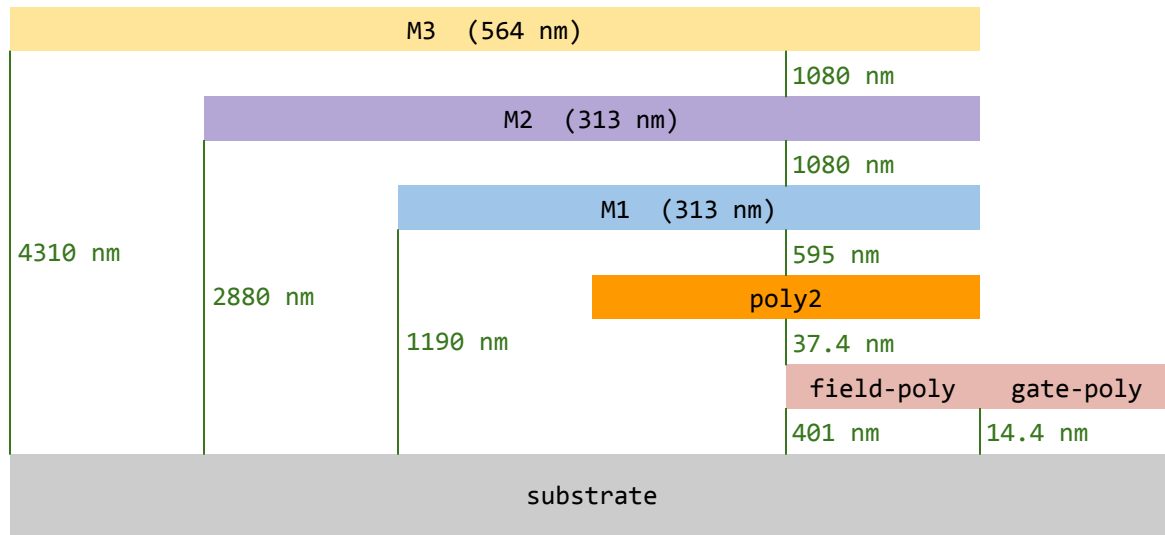


OnSemi C5N process layer stackup dimensions extracted from published data

\* thickness from resistance

\* distance from capacitance value



Dan White  
2016-04-23